## A Dual-Engine for Early Analysis of Critical Systems

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A Dual-Engine for Early Analysis

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### Motivation

- Alloy is a widely-used modeling language
  - Stand-alone framework for checking high-level system designs
    - Network protocols
    - File system policies
    - Schedulers
  - Intermediate language for checking programs
    - Functional properties of Java programs
    - Test case generation
    - Specification extraction
  - Engine for generating counterexamples
    - For theorem provers
- Taught in about 30 universities
- Used in industry (AT&T, Telcordia, etc.)

# Alloy for critical systems

- The intentional naming system for dynamic networked environments [Khurshid et. al, 2000]
- The NASA's Direct-To system for helping air traffic controllers [Ghassemi et. al, 2001]
- A role-based access control schema for sensitive resources [Zao et. al, 2003]
- A pull-based asynchronous rekeying framework in secure multi-cast [Taghdiri et. al, 2003]
- The Mondex electronic purse system [Ramananandro et. al, 2007]
- The New York City subway signaling system [Sarma et. al, 2008]
- The flash file system, responsible for NASA's mars rover breakdown [Kang et. al, 2008]
- The security domain model analysis for illicit information flows [Shaffer et. al, 2008]
- A constraint analysis on Java Bytecodes for security vulnerabilities [Reynolds et. al, 2010]

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### The Alloy language

- Declarative modeling language
  - Support for various abstraction levels
  - Various development/interest levels
- Simple, uniform semantics
  - Everything is a relation
  - Semantics equivalent to first-order relational logic
- Expressive, familiar syntax
  - Set and relational operators, first-order quantifiers, transitive closure, linear integer arithmetic
  - Concise formulation of rich properties
  - Syntax similar to an OO language
- Analyzable
  - The Alloy Analyzer (AA) is fully automatic
  - Looks for an instance violating a given assertion

# The Alloy Analyzer (AA)

- Performs bounded analysis
  - Requires a user-provided scope
  - Reduction to a satisfiability problem (SAT)
  - Enables automation



- Shortcomings
  - Can never prove an assertion correct, even for the simplest models
  - Limited support for numerical expressions
  - These are critical for critical infrastructures
  - $\triangleright$  Need for an automatic, proof-capable engine!

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#### Verification – Approach

Undecidable

- SMT engine [GT11]
  - No type finitization  $\Rightarrow$  Can prove valid assertions
  - Fully automatic
  - Full support of linear integer arithmetic
- Interactive theorem proving only as a last resort
  - Semi-decidable FOL version
  - Needs correctness confidence
- Bounded verification
  - Economical, and suitable for counterexamples
  - If unsat, provides confidence in correctness
  - $\Rightarrow$  The larger the bounds, the higher the confidence

#### Verification – Approach

#### A Dual-Engine

- Counterexample/Confidence:
  - SMT-based bounded analysis (UFBV): Decidable and fully automatic
  - Improvements in performance and scalability
- Verification:
  - SMT-based engine: not complete, but still fully automatic in many cases
  - ITP-based framework: in general interactive, but complete



#### Example - a simple file system

Alloy Model:

```
abstract sig FS0 {
parent: lone Dir
}
sig Dir extends FSO {
 contents: set FSO
}
sig File extends FSO {}
fact {
 contents = ~parent
 all d:Dir| not(d in d.^contents)
all d:Dir| #(d.contents) <= 5
}
assert oneLocation {
all o:FSO, lone d:FSO| o in d.contents
}
check oneLocation for 8
```

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#### Example – declarations

Alloy Model:





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SMT-based bounde	d Analysis – <i>type hierarchy</i>	
• Bitvector sorts for Alloy to	op-level types	
Membership function for A	Alloy types	
• FOL axioms to enforce sul	btyping [log <sub>2</sub> (  <i>FSO</i>  )] Z3 Spec:	
Alloy Model:	(define-sort () FOS BitVec[X])	
<pre>abstract sig FS0 {    parent: lone Dir }</pre>	( <b>declare-fun</b> isFSO (FSO) Bool) ( <b>declare fun</b> isDir (FSO) Bool)	
<pre>sig Dir extends FSO {     contents: set FSO</pre>	(declare-fun isFile (FSO) Bool)	
}	(assert (forall (f FS0)	
<pre>sig File extends FSO {}</pre>	(=> (isDir f) (isFSO f)))) (assert (forall (f FSO)	
	(=> (isFile f) (isFSO f))))	

## SMT-based bounded Analysis - type hierarchy

- Abstract types are the union of their subtypes
- Extension types are disjoint

Alloy Model:

```
Z3 Spec:
```

```
abstract sig FSO {
  parent: lone Dir
}
sig Dir extends FSO {
  contents: set FSO
}
sig File extends FSO {}
```

```
(assert (forall (f FS0)
 (=> (isFS0 f)
  (or (isDir f)(isFile f)))))
(assert (forall (f FS0)
  (not
     (and (isDir f)(isFile f)))))
```

# SMT-based bounded Analysis - relations

- Membership function and type-enforcing axioms for each relation
- · Uninterpreted functions to enforce multiplicity keywords



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## ITP – type hierarchy

- Sorts for relations and tuples: Relation, Tuple
- Membership predicate: in(Tuple,Relation)
- Constructor functions freely generate Tuple2, Tuple3, etc.: Tuple2 binary(Atom,Atom), Tuple3 ternary(Atom,Atom,Atom)
- Alloy's operators built explicitly higher level reasoning subset(Relation, Relation), Rel1 union1(Rel1, Rel1), Rel2 union2(Rel2, Rel2)
- Reasoning via sequent calculus (KeY):

$$\bigwedge \mathcal{M} \land \bigwedge \mathcal{F} \Rightarrow \mathsf{a} \; \rightsquigarrow \; \llbracket \mathcal{M} \rrbracket, \llbracket \mathcal{F} \rrbracket \vdash \llbracket \mathsf{a} \rrbracket$$

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## ITP – type hierarchy



```
Rel1 FSO; Rel1 Dir; Rel1 File;
Rel2 parent; Rel2 contents;
∀Atom this; (in(this,Object)
 \Rightarrow (in(this,File) | in(this,Dir))),
subset(parent,prod1x1(Object,Dir)),
∀Atom this; (in(this,Object)
 \Rightarrow lone(join1x2(sin(this), parent))),
subset(File,Object),
subset(Dir,Object),
subset(contents,prod1x1(Dir,Object)),
disj(File,Dir),
\vdash
```

. . .

# Proving Strategy

KeY's proof strategy extended for efficient relational reasoning

- Expand predicate invocations in the succedent to their definitions: universal quantification is eliminated by skolemization
- Keep predicate invocations in the antecedent; use lemmas to exploit their semantics, e.g.

```
assumes (subset(r,s) \vdash)
```

```
find (in(a,r) \vdash)
```

```
\add (in(a,s) \vdash)
```

- Lemmas capture simple properties of transitive closure, e.g. \find (in(binary(a,b),transClos(r)) ⊢) \add (∃Atom c; in(binary(c,b),r) ⊢)
- Simplification lemmas are applied greedily, e.g.

```
find (union1(r,r))
```

 $\$ replacewith (r)

+  $\sim$  500 provided and proved lemmas

#### Evaluation

		AA		BOUNDED Z3		UNBOUNDED Z3		KeY	
Property	Scope	TIME	RES	Time	Res	Time	Res	Step	Res
delUndoesAdd	16	0.8	CE	0.4	CE				
-Buggy	32	58	CE	1.0	CE	-	NA	-	NA
delUndoesAdd	32	150	BV	0.0	BV				
	64	то	UK	0.0	BV	0.0	FV	-	NA
lookupYields	8	101	BV	147	BV				
	16	ТО	UK	то	UK	то	UK	122	FV

		AA		bounded Z3		UNBOUNDED Z3		KeY	
Property	Scope	Time	RES	Time	Res	Time	Res	Time	Res
BuggyCOM	16	427	CE	3.6	CE				
Theorem 1	17	то	CE	1.9	CE	-	NA	-	NA
COM	16	451	BV	0.0	BV				
Theorem 1	17	то	UK	0.3	BV	0.0	FV	-	NA
mark sweep	9	140	BV	17	BV				
Soundness 1	10	то	UK	107	BV	то	UK	10	FV

CE: counterexample, BV: bounded valid, FV: fully valid, UK: unknown, NA: not applicable, TO: time out (> 10 min.)

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#### Related work

- Dynamite [Frias, Pombo, Moscato, 2007] uses PVS
- Prioni [Arkoudas, Khurshid, Marinov, Rinard, 2003] uses Athena
  - Based on interactive theorem provers
  - Interactive, regardless of the complexity of the problem
  - No support of integer or cardinality expressions
  - (Dynamite) Reduction to binary relations results in additional proof obligations
- SMT-based engine [El Ghazi, Taghdiri, 2011] uses Z3
  - Fully automatic proofs
  - support of integer and cardinality expressions
  - No completeness guarantee

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### Conclusions

- A dual engine capable of proving and refuting Alloy assertions
  - Via tool chain from fully automatic to interactive proving
  - Bounded SMT engine that improves AA counterexample/confidence
  - Unbounded SMT engine fully automatic proof capability
  - ITP framework in KeY- interactive but complete
- Reduces cost and increases flexibility
  - · Cost should depend on problem complexity
  - In earlier software development stages, flaws are expected

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